

ABSTRACT OF THE DISCLOSURE

A circuit apparatus of extending General Purpose Input Output pins, which includes a control processing unit, a memory, and a buffer. Wherein, data pins of the control processing unit is coupled to memory pins of the memory and those in the buffer. Input signals can be temporarily stored in the buffer when the buffer is used as a buffer for inputting signals. And the buffer can feed input signals into the control processing unit while the memory is being recharged. Likewise, if the buffer is used as a buffer for outputting signals, the buffer can receive output signals (So) from the control processing unit and output those signals while the memory is being refreshed.

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